ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention

SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE

Application Number:

Confirmation Number:

First Named Applicant: Victor Chan
Attorney Docket Number: FIS920030105

Search string: 1

(6245615 or 20010026006 or 20020014666 or 20020063292 or

20020185676).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
Po	1	6245615	2001-06-12	Noble et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
R	1	20010026006	2001-10-04	Noble et al.			
P	2	20020014666	2002-02-07	Ohmi et al.			
PB	3	20020063292	2002-05-30	Armstrong et al.			
a	4	20020185676	2002-12-12	Momose			

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			U.S. P/	ATENT DOCUMENTS					
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Po		5,317,175	05/31/94	Throngnumchai					
PC		2001/0026006 A1	10/04/01	Noble, et al.					
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